



Row Vector	Address		Bit Selection			
	A <sub>2</sub>	A <sub>1</sub>	A <sub>1</sub> = 0	A <sub>1</sub> = 1	A <sub>2</sub> = 0	A <sub>2</sub> = 1
RVO	0	0	x		x	
RV1	0	1		x	x	
RV2	1	0				x
RV3	1	1		x		x

↓

RCS0

↓

RCS1

↓

RCS2

↓

RCS3

FIGURE 4

Column Vector	Address		Bit Selection			
	A <sub>2</sub>	A <sub>1</sub>	A <sub>1</sub> = 0	A <sub>1</sub> = 1	A <sub>2</sub> = 0	A <sub>2</sub> = 1
CVO	0	0	X		X	
CV1	0	1		X	X	
CV2	1	0				X
CV3	1	1		X		X

↓

CCS0

↓

CCS1

↓

CCS2

↓

CCS3

FIGURE 5